



03500.016066 (35.C16066)

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	Examiner: Patrick J. Lee
MAMORU UCHIDA	)	Group Art Unit: 2878
Application No.: 10/025,928	)	
Filed: December 26, 2001	)	
For: SEMICONDUCTOR DEVICE,	)	
OPTOELECTRONIC BOARD,	)	
AND PRODUCTION METHODS	)	
THEREFOR	)	July 15, 2004

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the document listed below. A copy of the document, together with an English-language translation, is enclosed.

Office Action in Japan 2001-379831 (Dispatch Date: January 27, 2004)

The Japanese Office Action issued during prosecution of a Japanese patent application corresponding to the subject application. The document listed in the Japanese Office Action (Japan 61-28240) was cited in the June 7, 2004 Information Disclosure Statement.

### FORMAL MATTERS

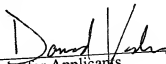
In accordance with 37 C.F.R. § 1.97(b), since a first Office Action on the merits of the subject application has not yet been issued, neither a statement under 37 C.F.R. § 1.97(e) nor payment of a fee is believed to be required for consideration of this Information Disclosure Statement. However, the Commissioner is hereby authorized to charge any fee which may be required in connection with this paper to Deposit Account No. 06-1205. A duplicate of this paper is enclosed for that purpose.

### CONCLUSION

It is respectfully requested that the above information be considered by the Examiner.

Applicants' undersigned attorney may be reached in our Washington D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our address given below.

Respectfully submitted,

  
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